



MJM Do. No. 5038-118

## MASS STORAGE CACHING PROCESSES FOR POWER REDUCTION

**ABSTRACT** 

•

A memory system with minimal power consumption. The memory system has a disk memory, a non-volatile cache memory and a memory controller. The memory controller manages memory accesses to minimize the number of disk accesses to avoid the power consumption associated with those accesses. The controller uses the cache to satisfy requests as much as possible, avoiding disk access.



10